

# Marvell® Brightlane™ 88Q6113 Secure Multi-Gig Aggregation Automotive Switch

11-port Automotive Ethernet Switch with Advanced Security & Support for TSN

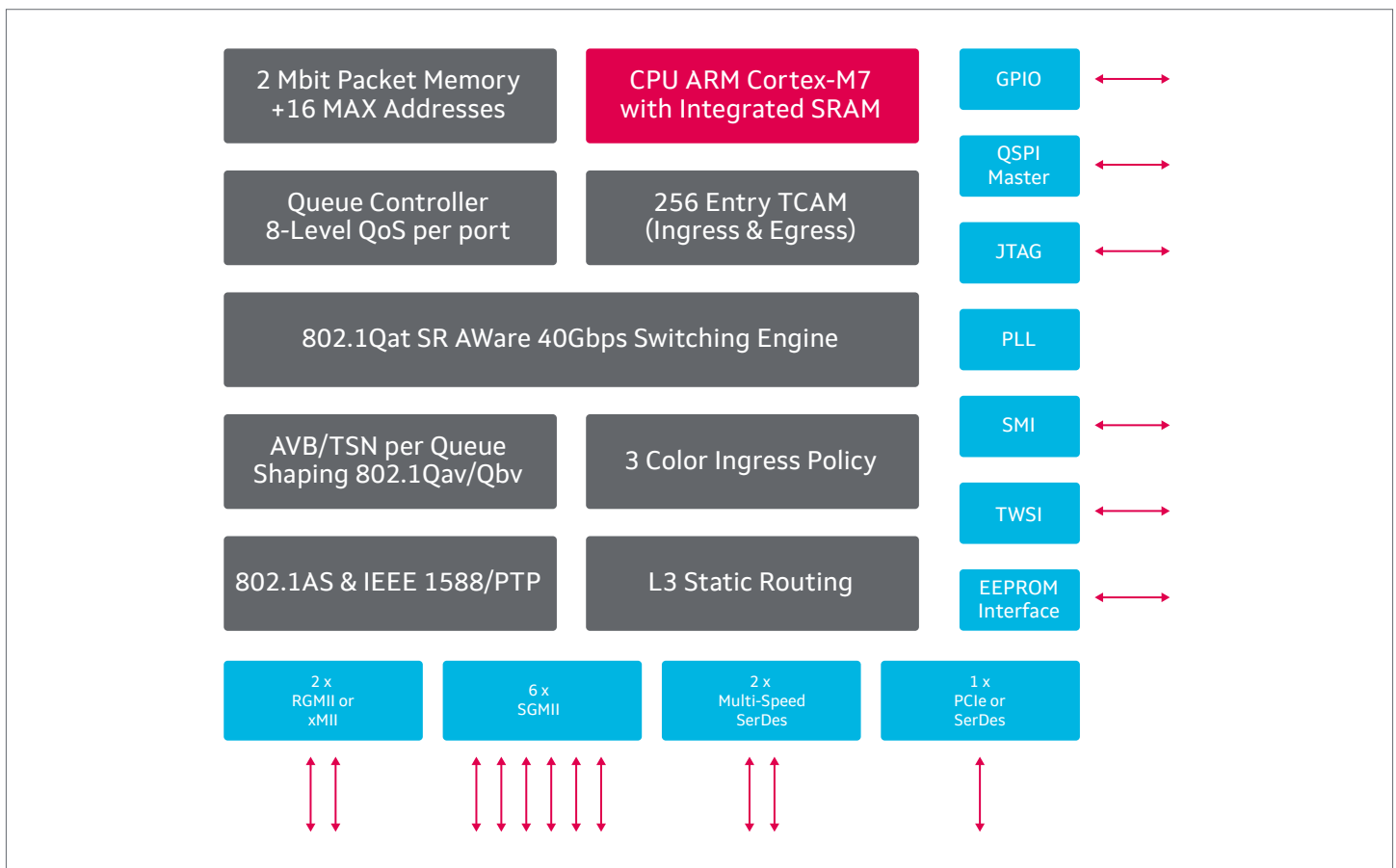
## Overview

Marvell's Brightlane™ first generation high-port count Automotive Ethernet switch, 88Q6113, is an 11-port aggregation ethernet gigabit capacity switch that is fully compliant with IEEE802.3 automotive standard, delivering high bandwidth with advanced routing and security.

The 11-port Ethernet switch offers all ports at gigabit capacity MAC interfaces with 2x RGMII or MII/RMII/GMII, 6x SGMII, 2x XFI Multi-Speed SerDes (1/2.5/5/10 Gbps) or SGMII port, MII/RMII/GMII, 1xPCIe or SerDes. The switch offers local and remote management capabilities, providing easy access and configuration of the device.

This switch includes a high-performance ARM® Cortex M7 CPU with dedicated on-chip memory to support AVB protocols such as Precision Time Protocol (PTP) and security firewall to protect from external malicious attacks. The switch employs advanced routing engine to support Gigabit routing of the incoming packets without the CPU intervention. It offers highest level of security with deep packet inspection (DPI) techniques, Denial of Service (DoS) engine and Trusted Boot functionality to secure the vehicle networks.

## Block Diagram



Marvell Brightlane™ 88Q6113 Block Diagram

## Key Features

Features	Benefits
Processor	<ul style="list-style-type: none"><li>• Integrated ARM Cortex-M7 CPU, 350MHz with 1MB SRAM</li></ul>
IO Interfaces	<ul style="list-style-type: none"><li>• 2x RGMII or MII/RMII/GMII ports</li><li>• 6x SGMII</li><li>• 2x XFI (up to 10Gbps (1000BASE-X/SGMII, 2500BASE-X, 5GBASE-R, 10GBASE-R, USXGMII))</li><li>• 1x PCIe (Gen 3x2, 16Gbps) or SerDes (1/2.5Gbps)</li><li>• 2x SMI<ul style="list-style-type: none"><li>• Master interface to connect to external PHYs or additional switches</li><li>• Slave interface to manage the switch</li></ul></li><li>• Configurable GPIOs</li><li>• QSPI with configurable frequencies (19.2MHz-83.3MHz)</li><li>• TWSI Master interface</li><li>• JTAG Interface for debugging</li></ul>
Package Characteristics	<ul style="list-style-type: none"><li>• 17mmx17mm, 356-pin ball array TFBGA, 0.8 mm pitch</li></ul>
EEPROM	<ul style="list-style-type: none"><li>• Slave interface with loader to configure the switch (32Kb-512kb)</li></ul>

## Target Applications

- Autonomous Drive
- Advanced Driver Assistance Systems (ADAS)
- Central Gateway



Marvell first revolutionized the digital storage industry by moving information at speeds never thought possible. Today, that same breakthrough innovation remains at the heart of the company's storage, networking and connectivity solutions. With leading intellectual property and deep system-level knowledge, Marvell semiconductor solutions continue to transform the enterprise, cloud, automotive, industrial, and consumer markets. For more information, visit [www.marvell.com](http://www.marvell.com).

© 2020 Marvell. All rights reserved. The MARVELL mark and M logo are registered and/or common law trademarks of Marvell and/or its Affiliates in the US and/or other countries. This document may also contain other registered or common law trademarks of Marvell and/or its Affiliates.